MVME-1150 64-bit Optically Isolated Digital Input Module

Specifications

Number of Digital Input Channels **-** 64

Input Voltage Options - 5V, 12V, 24V, or 48V.

Digital Filter - Jumper selectable - 16 options
1.25usec to 40.96ms

Base Address and Control and Status Register -
Base address, Short I/O Access, set by DIP switches.

Input Type – Voltage input or contact sense. Positive or negative true logic. All set by DIP switches.

BIT Built-In-Test – All circuit functions are tested via the BIT feature; excluding the output driver transistor

Visual Indicator – Front Panel LED to monitor write data functionality

VME Specifications

Meets VMEbus Specifications Revision C.1 IEEE Std. 1014-1987, IEC 821, IEC 297
A16:D16/D08 (EO) :Slave:39/3D:29/2D 1K consecutive byte locations, base address configurable within 64K Short I/O space.
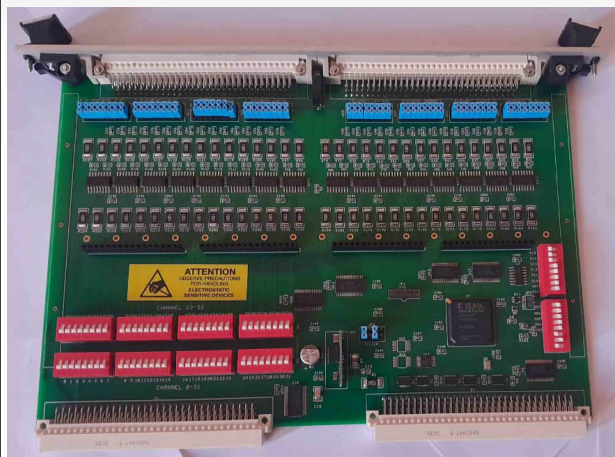
Board Size - 6U

Power Requirements

+5 VDC @ 1.0 A, typical

Environmental

Operating Temperature: -20 to 85 °C
Storage Temperature: -40 to 125 °C
Shock: 25g, 11ms on all axis

The MVME-1150 offers the following features:
The Merlin product is a drop-in replacement for the GE/Abaco VMIVME-1150. It works with the customer's existing software and cabling. No changes are required.

The MVME-1150 offers the following improvements over the original VMIVME-1150:

- Input filter is jumper selectable.
- User can configure inputs for either voltage input or contact sense.
- Positive or negative true logic is jumper selectable.

As with all Merlin Embedded DIR products, the MVME1150 has the same, or better, functional performance than the product it is replacing. All Merlin Embedded products are backed by a 2-year warranty and 15 years of life-cycle support. The user will not have to take on any obsolescence issues when utilizing Merlin Embedded products.
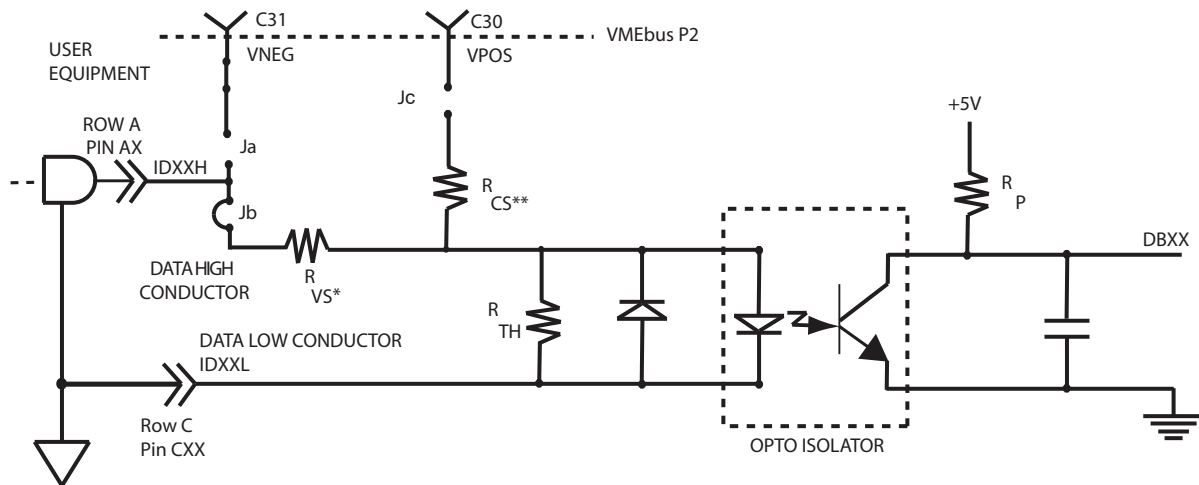
Merlin Embedded
10415 Willow Ridge Loop
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www.merlinembedded.com
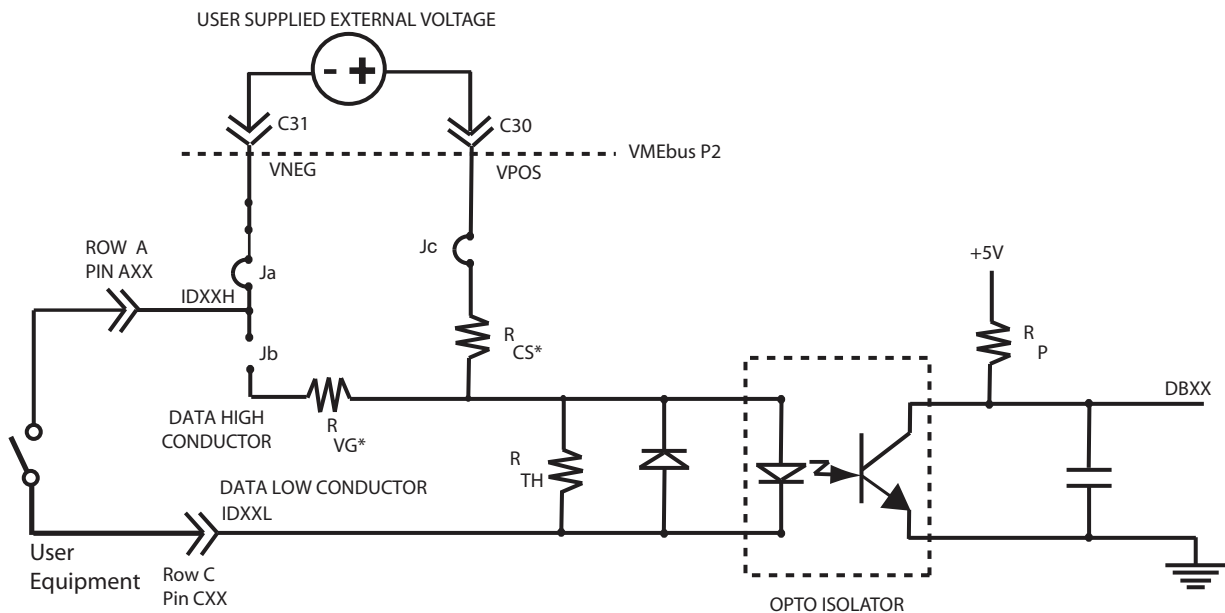**866-700-7704** Fax: 407-574-5694
sales@merlinembedded.com

1. Ground IDXXL (XX = Channel Number)
2. Connect Data Input to IDXXH (XX= Channel Number)
3. Jb connection made by placing jumper on input header Ux pins 1-2.
   * Resistor values dependent upon input voltage range.

Figure 1. Voltage Source Operation

1. Connect contacts such that it is across IDXXH and IDXXL (XX = Channel Number)
2. Ja connection made by placing jumper on input header Ux pins 2-3.
3. Jc connection made by switch Sx in the ON Position

* Resistor values option dependent (see specification sheet).

Figure 2. Contact Sense Operation

1. Connect the input such that it is across IDXXH and IDXXL (XX = channel number).
2. Ja connection made by placing jumper on input header Ux pins 2-3.
3. Jc connection made by switch Sx in the ON Position

* Resistor values option dependent (see specification sheet)

Figure 3. Logic Source Operation (Not Directly TTL Compatible)